

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,130,224 B2

Page 1 of 1

APPLICATION NO. : 10/522316

DATED : October 31, 2006

INVENTOR(S) : Katsutoshi Moriyama et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

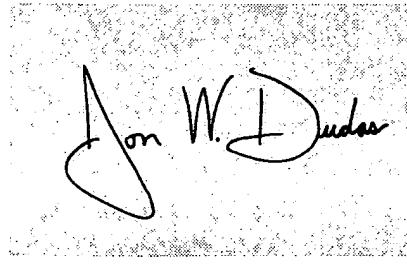
Title Page:

Item (54) should be read as follows:

-- COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE
INCLUDING COMPOUND STORAGE CIRCUIT --.

Signed and Sealed this

Eighteenth Day of December, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office